## PATENT ABSTRACTS OF JAPAN

(11)Publication number :

04-368065

(43)Date of publication of application: 21.12.1992

(51)Int.CI.

H04N 1/40 G06F 15/64 G06F 15/68 G06K 9/38 H03K 5/08 H03M 1/18 // H04N 7/13

(21)Application number: 03-144428

(71)Applicant: **NEC CORP** 

(22)Date of filing:

17.06.1991

(72)Inventor:

**NEC ROBOTICS ENG LTD UMEDA TETSUO** 

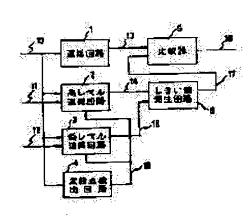
**MUTO HIROYUKI** WATARAI HIROKAZU KOBAYASHI HIRAO

## (54) VIDEO SIGNAL BINARIZING CIRCUIT

(57)Abstract:

PURPOSE: To convert a video signal of information described on paper such as a character or an image into a binary signal.

CONSTITUTION: A high level tracking circuit 2 tracking a high amplitude level of a video signal, a low level tracking circuit 3 tracking a low level of a video signal, and an inflection point detection circuit 4 detecting a vertical change point of the amplitude are used to track high and low levels of the video signal and a threshold level generating circuit 6 obtains a signal of an intermediate level from the two level signals. A comparator 5 receives a delay video signal outputted from a delay circuit 1 and an output signal of the threshold level generating circuit 6 to obtain a binarized video signal. Thus, the binarized video signal with less distortion and without being affected of the level of the amplitude of the video signal is obtained.



## **LEGAL STATUS**

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection],

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office